

Semiconductor Weekly News

September 7 - 13, 2026

The long-range High NA roadmap took shape, big money backed the memory constraint, and a US state and a US state moved first on AI rules.

Miki Matsui, Co-Founder, CHIP-J

Sources: ASML, NVIDIA, Apple, NIST / US Commerce, Office of the Governor of California, Lyft, PR Lyft, PR Newswire, CNBC, TechCrunch



Editor's Picks

2033

ASML and TSMC TSMC go 12-inch inch

They formed an industry initiative to move High NA EUV to 12-inch photomasks, targeting a pilot line by 2031 and production readiness by 2033. ([ASML, Sep 8](#))

\$875M

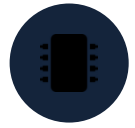
Positron AI raises at \$5B

Its Asimov inference chip uses commodity LPDDR5X instead of HBM, taping out on TSMC N3P at end-2026 for production in H2 2027. ([PR Newswire, Sep 10](#))

First in US

California sets AI audit standards

Governor Newsom signed SB 813 and AB 1405, the first state standards for independent verification organizations and a registry of AI auditors. ([Governor's Office, Sep 9](#))



1. AI Chips & GPUs

- **Huang voices confidence in 70% growth**

At the Goldman Sachs conference he said he is confident of 70% revenue growth next year, with unconstrained demand growth above 100%, while acknowledging supply-chain, land and power limits. This is a CEO view rather than formal guidance. ([TechCrunch, Sep 10](#))

- **d-Matrix adopts NVLink Fusion**

The inference chipmaker will connect its next-generation Raptor XPUs to NVIDIA's platform via NVLink Fusion. A competing architecture inherits NVIDIA's scale-up networking rather than building a fabric of its own. ([NVIDIA, Sep 10](#))

- **NVIDIA and Palantir partner on sovereign AI**

The two will apply sovereign AI to critical supply chains, with NVIDIA's own operations as the first deployment. It pairs compute with a data platform for markets that require domestic control of models and data. ([NVIDIA, Sep 10](#))

- **Skild AI's S1 learns from a single video**

Built on NVIDIA's physical-AI stack, S1 lets industrial robots pick up new tasks from one demonstration video instead of bespoke reprogramming. It is a fresh source of demand for edge inference silicon. ([NVIDIA, Sep 10](#))



2. Fab & Supply Chain

- **ASML and TSMC lead the 12-inch mask shift**

Ahead of the SPIE BACUS conference they formed an industry initiative on September 7 to move from 6-inch to 12-inch photomasks, targeting a pilot line by 2031 and production readiness by 2033. TSMC intends to use High NA in high-volume advanced-node manufacturing from 2030. [\(ASML, Sep 8\)](#)

- **TSMC and Samsung commit to High NA EUV**

Both said they will adopt ASML's High NA EUV, following Intel. Samsung is targeting DRAM manufacturing by 2028. High NA will run on today's 6-inch masks for now, with the 12-inch format positioned as the later productivity step. [\(CNBC, Sep 8\)](#)

3. Consumer Chips

- **Apple puts the 2nm A20 Pro in iPhone Duo**

The first foldable iPhone starts at \$1,999 for 256GB. The A20 Pro is built on 2nm: a 6-core CPU up to 20% faster than A19 Pro, a 7-core GPU up to 40% faster, a new Dual 16-core Neural Engine with 2x the compute, and 50% more unified memory bandwidth. Pre-orders Oct 16, availability Oct 23. ([Apple, Sep 9](#))

- **Consumer memory stays at elevated prices**

With memory makers reallocating lines to HBM for AI, commodity DRAM and NAND remain tight and retail DDR5 and DDR4 pricing carried August's highs into September. PC and smartphone makers are bidding against AI infrastructure for the same supply. ([Tom's Hardware index, week of Sep 7](#))



4. Data Centers & Energy

- **NVIDIA secures land and power in Australia**

NVIDIA is working with Australian Cloud Partners to lock up land, power and shell capacity able to host multiple generations of DSX systems. Structuring around land-and-power rather than a single deployment hedges the siting and grid limits now binding in the US. [\(NVIDIA, Sep 9\)](#)

- **OpenAI pauses new Pro subscriptions**

OpenAI halted new Pro-tier signups, saying Astra demand outpaced serving capacity — a rare public admission that inference supply, not demand, is the constraint. Chip and memory scarcity now shapes consumer product availability. [\(TechCrunch, Sep 10\)](#)



5. Autonomous Driving & Auto Chips

- **Waymo rides go live on the Lyft app**

Nashville is the first city where riders can hail a Waymo from the Lyft app. Lyft's Flexdrive handles vehicle readiness, charging, cleaning and maintenance from a purpose-built 80,000-square-foot depot opening in October, adding more than 70 full-time roles. [\(Lyft, Sep 9\)](#)

- **Robotaxi leaders converge on NVIDIA**

NVIDIA said every major robotaxi program running at commercial scale uses its three-computer stack for training, simulation and in-vehicle compute. Citing a Goldman Sachs projection, it put the market at \$400 billion by 2035 with over six million commercial vehicles. [\(NVIDIA, Sep 10\)](#)

- **Tesla sets October 1 for the Roadster**

Tesla confirmed an October 1 unveiling for the long-delayed second-generation Roadster, adding a dated milestone while its Cybercab rollout remains under federal investigation. Its AV programs deepen reliance on in-house inference silicon. [\(TechCrunch, Sep 12\)](#)

6. Chip Startups & Venture

\$875M

Positron AI at a \$5B valuation

A \$375M Series C at \$3.5B pre-money co-led by NEA and others, plus a Series C-1 of up to \$500M led by NEA and Jim Clark. Asimov carries 288GB-2,304GB of LPDDR5X per chip, sidestepping HBM and CoWoS supply. ([PR Newswire, Sep 10](#))

\$468M

Kepler Computing exits stealth

After seven years it unveiled 3D-stacked ferroelectric memory. Funding totals \$468M to date from GlobalFoundries (\$50M), Intel Capital, AMD Ventures, Baillie Gifford and Gates Frontier, plus a CHIPS letter of intent up to \$245M. ([Hoodline, Sep 9](#))

IPO track

Nscale adds Fidji Simo to its board

The London-based AI data-center operator appointed Fidji Simo, formerly CEO of AGI Deployment at OpenAI and previously CEO and Chair of Instacart through its 2023 listing, to its board effective immediately. ([PR Newswire, Sep 11](#))

\$3B

The Boring Company raises, UAE-led

Musk's tunnelling company closed \$3 billion led by UAE investors, showing Gulf capital taking lead roles in capital-intensive US hardware rather than passive positions — the same pools funding AI data centers and chip ventures. ([TechCrunch, Sep 10](#))



7. Four CHIPS Quantum Awards

On September 8 the US Commerce Department signed final CHIPS R&D awards with four quantum computing companies, each up to \$100 million (up to \$400 million in total), routing quantum money through semiconductor process infrastructure

PsiQuantum

Addresses photonic quantum computing challenges and integrates them into semiconductor manufacturing: electro-optic materials, high-temperature single-photon detectors and ultra-low-loss photonic packaging. ([NIST, Sep 8](#))

Quantinuum

Targets technology and quantum semiconductor manufacturing bottlenecks for scaling fault-tolerant trapped-ion quantum computers. ([NIST, Sep 8](#))

D-Wave

Funds critical advancement of semiconductor process technologies for annealing and gate-model superconducting quantum computing systems. ([NIST, Sep 8](#))

Rigetti

Addresses challenges in developing and scaling next-generation superconducting quantum computing: readout electronics, cryostat architectures and fabrication for high-connectivity chip architectures. ([NIST, Sep 8](#))



8. California AI Regulation

No new Section 232 tariff action landed in the window, while California moved first in the nation on AI oversight.

First-in-the-nation state AI audit standards

Governor Newsom signed SB 813, a framework for independent verification organizations that assess AI systems for compliance with state law, and AB 1405, creating a state registry of AI auditors with standards for independence and transparency. The bills build the foundation for third-party evaluation rather than imposing a blanket audit mandate. [\(Governor's Office, Sep 9\)](#)

Bipartisan child-safety rules for chatbots

A day later he signed what the state calls the nation's strongest companion-chatbot safety rules, plus a ban on addictive social media features for minors. The bipartisan package puts the burden on operators to build minor protections into AI conversations. [\(Governor's Office, Sep 10\)](#)

AskCA launches as a state AI service guide

Delivered under the March 2026 AI executive order, AskCA helps residents navigate state services. California is regulating AI and procuring it at the same time. No semiconductor manufacturing or fab-siting announcements were issued during the window. [\(Governor's Office, Sep 9\)](#)



Sources & Verification

- ASML (Sep 8 press release) - 12-inch mask initiative, TSMC High NA from 2030
- NVIDIA Newsroom / Blogs (4) - d-Matrix, Palantir, Skild AI, Australia, robotaxi stack
- Apple Newsroom (Sep 9) - iPhone Duo and A20 Pro specifications and pricing
- NIST / US Commerce (3, Sep 8) - final CHIPS R&D awards to PsiQuantum, D-Wave, Quantinuum
- Office of the Governor of California (3, Sep 9-10) - AI audits, child safety, AskCA
- PR Newswire (Positron Sep 10, Nscale Sep 11), Lyft IR (Sep 9), CNBC, TechCrunch, Tom's Hardware

Every item was checked against a primary source or a named outlet. Out-of-window, mis-attributed and speculation-only items were removed.

Miki Matsui, Co-Founder, CHIP-J